

My residence, post office address and citizenship are as stated below next to my name;

METHOD AND SYSTEM FOR ANALYZING QUIESCENT POWER PLANE CURRENT (IDDQ) TEST DATA IN VERY-LARGE SCALE INTEGRATED (VLSI) CIRCUITS

 X is attached hereto.
 was filed on
as Application Serial No.
and was amended on
(if applicable)

I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56, including for continuation-in-part applications, material information which became available between the filing date of the prior application and the national or PCT international filing date of the continuation-in-part application.

I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or (f), or 365(b) of any foreign application(s) for patent, inventor's or plant breeder's rights certificate(s), or 365(a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below, any foreign applicaiton for patent inventor's or plant breeder's rights certificate(s) or any PCT international application having a filing date before that of the application on which priority is claimed.

Priority Claimed

Certified Copy Attached?
 Yes No

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorneys and/or agents to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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